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Docket No.: 60188-400

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277
TAMAKI, TOKUHIKO, et al. : Confirmation Number: 1307
Serial No.: 09/964,868 : Group Art Unit: 1765
Filed: September 28, 2001 : Examiner: DUY VU NGUYEN DEO

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For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD FOR
FABRICATING THE SAME

INFORMATION DISCLOSURE STATEMENT

Mail Stop DD
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.

CERTIFICATION PARAGRAPH

The undersigned certifies that, to the best of his knowledge, each item of information contained in this Information Disclosure Statement was first cited in a communication from a

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foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement as described in 37 CFR 1.97(e)(1).

Each non-English language reference was cited in a corresponding foreign application search report or office action and its relevance discussed therein. A copy of the foreign search report or office action, together with an English language version thereof, is attached for the Examiner's information.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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FROM:

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Client/Matter/Tkpr:	60188-400-5328	Date:	April 6, 2004
		Time Sent:	
		Number of pages including this page:	4.5

TO:

Name:	Nadine Norton	Facsimile No.	703-872-9306
Company:	U.S. Patent and Trademark Office	Contact No.	571-272-1465

RE: U.S. Patent Application No. 09/964,868
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD FOR FABRICATING THE SAME
Inventors: Tokuhiko TAMAKI, et al.
Our Reference: 60188-400
ISSUE FEE DUE: JUNE 11, 2004

Dear Ms. Norton:

Enclosed is the PTO 1449 form, Information Disclosure Statement (with date-stamped postcard) that was filed February 26, 2004. The Notice of Allowance was issued March 11, 2004 but does not consider the IDS filed February 26, 2004 and it is not in the PAIR system. Please have the appropriate Examiner consider this IDS and issue a supplemental paper acknowledging the IDS and references. If you have any questions on the above, please don't hesitate to contact me

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